

Dr Bishwajeet Kumar Pandey, SMIEEE



Associate Professor-Faculty of Engineering and Technology, Jain (Deemed-to-be University)

Senior Member, Institute of Electrical and Electronics Engineers (IEEE), USA

Life Member, Computer Society of India (CSI), India

Associate Member, The Institution of Engineers (IEI), India

Associate Member, The Institution of Electronics and Telecommunication Engineers, India

PhD (Gran Sasso Science Institute, L'Aquila, Italy)

M.Tech (Indian Institute of Information Technology, Gwalior, India)

Visiting Professor:UCSI University Malaysia and Aalborg University, Denmark

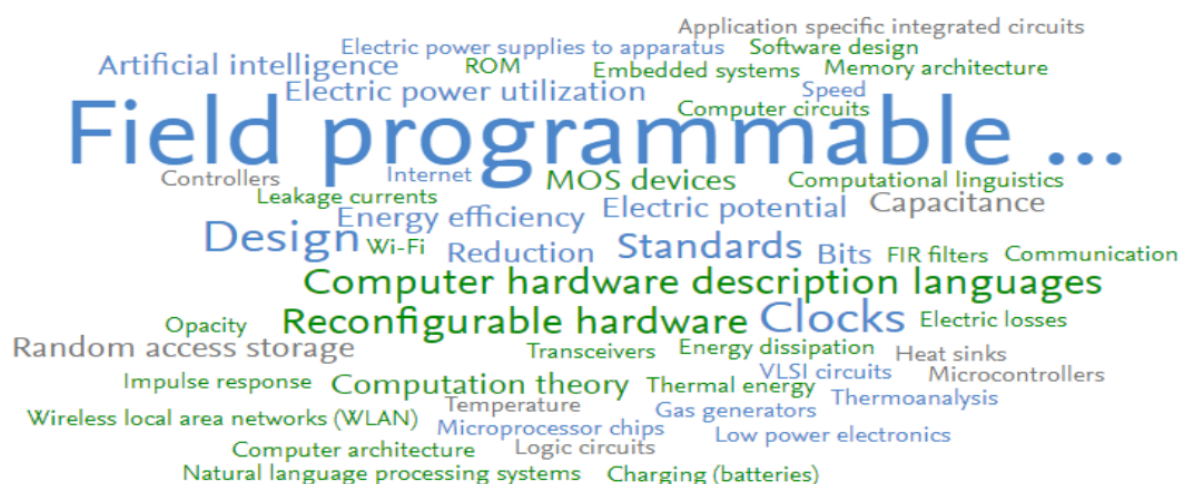
Scopus Profile: <https://www.scopus.com/authid/detail.uri?authorId=57203239026>

General Chair: RTCSE-2023 University of Hawaii USA, ICAIC-2022 University of Houston, USA,

IMCES-2022 Aalborg University Denmark,

PC Chair: BMESS-2022 Bath University UK-UAE Campus, ICGCET-2022 Mauritius,

My Research Areas; Automatically Generated by SCOPUS



Refer: <https://www.scopus.com/authid/detail.uri?authorId=57203239026>

Career Highlights

- **150+ in Scopus:** <https://www.scopus.com/authid/detail.uri?authorId=57203239026>
- **1800+ Citation:** https://scholar.google.co.in/citations?user=UZ_8yAMAAAAJ&hl=en
- **H-Index: 21**
- **25+ Papers Published in SCIE Index Journal**
- **Guest Editor of 6 Special Issues of ESCI Index Journal**
- **Organized 17 Conferences in 10 Countries**
- **Editors of 17 International Conference Proceedings**
- **Participated in 80 Conferences in 23 Countries**
- **2 Patents**
- **5+ Years Research Experience (2015-20, 2021) excluding Consultancy experience**
- **7+ Years Teaching Experience (2009-15, 2020-21, 2021-) excluding Visiting positions**
- **Graduate Aptitude Test for Engineering (GATE) in 2009, 2011, 2012, and 2013**
- **University Grant Commission (UGC-NET) (National Eligibility Test for Lectureship in India) in 2013**
- **UGC-JRF in 2013**

Certification & Award

- **IBM Certified DB2 Academic Associate: DB2 Database and Application Fundamentals**
- **IBM Certified Solution Designer: Rational Functional Tester for Java**
- **IBM Rational Software Engineering for Educational Development (SEED) Program**
- **EC Council Certified Ethical Hacker**
- **Young Scientist Award at Denmark in 2016**
- **Best Paper Award in RTCSE-2020 USA, IMCES-2019 Mauritius, ICGCET-2018 Morocco, CICN-2014 India and ICCCV-2013 India Conference.**
- **Excellent Oral Presentation in ICNCS-2013 Singapore Conference.**

Research Experience

Name of the Organization: Gyancity Research Consultancy Pvt Ltd, India

Duration: August 2021 To November 2021

Designation: Research Scientist/Consultant

Job Responsibility: Provide consultancy for organization of international conference, and international research project.

Name of the Organization: Gyancity Research Consultancy Pvt Ltd, India

Duration: August 2019 to August 2020 Designation: Research Scientist/Consultant

Job Responsibility: concerned with organization of research workshop in technical university in India and abroad, continue ongoing joint project with GSSI Italy in CPS, University of Hawaii USA in Disaster Management, and Aalborg University Denmark in Energy Technology.

Name of the Organization: Gran Sasso Science Institute, L'Aquila, Italy

Duration: November 2015 to July 2019 Designation: Researcher Scholar

Job Responsibility: concerned with models, algorithms, and software methodologies to successfully face the present and future challenges and also the design of systems that will need on physical entities and interact with virtual ones.

Name of the Organization: Centre for Development of Advance Computing (C-DAC), B-30, Sec-62, Noida, India

Project Name: Design and Implementation of an 8-bit Processor on FPGA

Duration: Sep 2012 to May 2013.

Job Responsibility: I was a part of Design and Implementation of 8-bit microprocessor-having microprogrammed architecture- on an FPGA. Micro programmed architecture includes Assembler, Editor, Downloader, Debugger and GUI. It was implemented for training purpose of IIT/IIM classroom after getting approval from the board of directors of IIT and IIM.

Teaching Experience

Name of the Organization: Jain University, Bangalore, India

Duration: December 2021 to Now Designation: Associate Professor in CSE

Job Responsibility: Taking Ethical Hacking Course for Bachelor and Master Students along with responsibility of Virtualization of University Activities.

Name of the Organization: UCSI University, Kuala Lumpur, Malaysia

Duration: December 2020 to December 2021 Designation: Adjunct Professor in CSE

Job Responsibility: Take courses for Institute of Computer Science and Digital Innovative of UCSI University, Kuala Lumpur Campus.

Name of the Organization: Birla Institute of Applied Science, Bhimtal, India

Duration: August 2020 to July 2021 Designation: Assistant Professor in CSE

Job Responsibility: Taught computer network, innovations and entrepreneurship. Supervised research (at beginner stage) of my students and targeting IEEE conference and conference in USA, Indonesia and Peru. After writing 1 research paper in conference, Supervised research (at expert stage) of my students and target IEEE Journals and Transactions, Science Direct Journals for 2nd paper of my students.

Name of the Organization: Aalborg University, Esbjerg, Denmark

Duration: 2016 to 2017 Designation: Visiting Professor

Job Responsibility: Taken courses (2 times) related to Energy Efficient Design using VHDL and Xilinx at Department of Energy Technology, Aalborg University, Denmark

Name of the Organization: Chitkara University, Chandigarh, India

Duration: Jul-2014 to Aug-2015 Designation: Assistant Professor in CSE/ECE

Job Responsibility: I do not only teach and advising but also discovery, creativity, economic

development and technology transfer. In addition to that, I serve on committees, write and edit research papers and submit grant proposals. I have supervised thesis of Master of Engineering students and project of Bachelor of Engineering students.

Name of the Organization: South Asian University, New Delhi, India

Duration: July 2013 to June 2014 Designation: Teaching Assistant

Job Responsibility: assist the university in their academic work, including tutorials, and supervision of lab work.

Name of the Organization: IIT, Gwalior, India

Duration: July 2011 to June 2013 Designation: Teaching Assistant

Job Responsibility: make theory lecture slide for my thesis supervisor, take lab class and take attendance in both theory and lab class.

Name of the Organization: IGNOU, New Delhi, India

Duration: July 2009 to June 2011 Designation: Lecturer in MCA

Job Responsibility: C, Java, Web of Programming, Theory of Compute Science, and DBMS

Courses Taught

M. Tech	Ethical Hacking, System Verilog, Logic Synthesis
B. Tech	Cloud Migration, Ethical Hacking, Application and Web Security, Computer Network, Digital. Electronics, Innovation and Entrepreneurship
MCA	Web Programming, Java, C, Theory of Computer Science, DBMS

Administrative Experience

Name of the Organization: Birla Institute of Applied Science, Bhimtal, India

Duration: August 2020 to July 2021 Designation: Head of International Liaison Committee

Job Responsibility: Sign MoU with Foreign University for following tasks:

1. Joint publications in reputed journals and conferences
2. Joint research guidance at either place in area of specialization
3. Working towards patenting of ideas
4. Joint sponsored projects from various funding agencies
5. Organizing joint conferences
6. Offering submodules/units of syllabi/modules from both partners
7. Student exchange with possible third-party funding
8. Curricula development at regular interval
9. Joint projects for UG and PG students
10. Development of a joint degree after matured interactions
11. Utilisation of laboratory facilities by students of both partners at both places
12. Joint international summer schools in possible topics of interest, either online/face-to-face

Technical Skills

Tools: <ul style="list-style-type: none">● NMAP, WIRESHARK, METASPLOIT● NESSUS, RECON-NG, MALTEGO● AWS, AWS Marketplaces,● Weka, Chidamber and Kemerer Java etrics (CKJM)● Raspberry Pi, Arduino, and Niagara● Xilinx ISE, Vivado ISE● Packet Tracer	Programming Languages: <ul style="list-style-type: none">● Python● C, C++● R● Java● HTML, CSS● VHDL, Verilog.
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Industry Experience

Name of the Organization: Web Sanchar India

Duration: May 2010 to June 2011

Designation: Web Manager

Job Responsibility: provide service of domain registration, web hosting services like managing Virtual Private Server.

Guest Editor of May 2021 Special Issue with Prof James Swart, Central University of Technology, South Africa

Special Issue Website:

<https://www.3ciencias.com/revistas/revista/3c-tecnologia-edicion-especial-mayo-2021/>

Journal Name: **3 C Tecnologia**

Publisher: Área de Innovación y Desarrollo, S.L. C/ Els Alzamora 17, Alcoy, Alicante, Spain.

Indexing: ESCI, EBSCO, DOAJ, Google Scholar, MIAR, REDIB, CSIC, CIRC...

Guest Editor of January 2021 Special Issue with Prof Ciro Rodriguez of Universidad Nacional Federico Villarreal, Lima, Peru

Special Issue Website:

<https://www.3ciencias.com/revistas/revista/3c-empresa-edicion-especial-enero-2021/>

Journal Name: **3 C Empresa**

Publisher: Área de Innovación y Desarrollo, S.L. C/ Els Alzamora 17, Alcoy, Alicante, Spain.

Indexing: ESCI, EBSCO, DOAJ, Google Scholar, MIAR, REDIB, CSIC, CIRC...

Guest Editor of November 2020 Special Issue with Prof Doris Esenarro Vargas, Federico Villarreal National University, Peru

Special Issue Website:

<https://www.3ciencias.com/revistas/revista/3c-tecnologia-edicion-especial-special-issue-noviembre-2020/>

Special Issue PDF:

<https://www.3ciencias.com/wp-content/uploads/2020/11/ed-esp-oct-2020-3c-tecno.pdf>

Journal Name: **3 C Tecnologia**

Publisher: Área de Innovación y Desarrollo, S.L. C/ Els Alzamora 17, Alcoy, Alicante, Spain.

Indexing: ESCI, EBSCO, DOAJ, Google Scholar, MIAR, REDIB, CSIC, CIRC..

Guest Editor of April 2020 Special Issue with Prof Jason Levy of University of Hawaii USA

Special Issue Website:

<https://www.3ciencias.com/revistas/revista/3c-tecnologia-edicion-especial-special-issue-abril-abril-2020/>

Journal Name: **3 C Tecnologia**

Publisher: Área de Innovación y Desarrollo, S.L. C/ Els Alzamora 17, Alcoy, Alicante, Spain.

Indexing: ESCI, EBSCO, DOAJ, Google Scholar, MIAR, REDIB, CSIC, CIRC...

Guest Editor of November 2019 Special Issue with Prof Jason Levy of University of Hawaii USA

Special Issue Website:

<https://www.3ciencias.com/revistas/revista/3c-tecnologia-ed-esp-noviembre-2019/>

Journal Name: **3 C Tecnologia**

Publisher: Área de Innovación y Desarrollo, S.L. C/ Els Alzamora 17, Alcoy, Alicante, Spain.

Indexing: ESCI, EBSCO, DOAJ, Google Scholar, MIAR, REDIB, CSIC, CIRC...

Selected 20+ Publication in SCIE Index Journal

1. **B. Pandey**, N. Bisht, S. Budhani, “Energy Efficient Adaptive AES Design on FPGA for Real Life Implementation”, Elsevier, Journal of Information Security and Applications (**Accepted**)
2. M K Hasan, T. M. Ghazel, R. A. Saeed, **B. Pandey**, H. Gohel, A. A. Eshmawi, S. Abdel-Khalek, H. M. Alkhassawneh, “A review on security threats, vulnerabilities, and counter measures of 5G enabled Internet-of-Medical-Things”, IET Communications, Nov 2021, <https://doi.org/10.1049/cmu2.12301>
3. M K Hasan, Musse Mohamud Ahmed, **B. Pandey**, H Gohel, Shayla Islam and Izzul Fitrie Khalid, “Internet of Things Based Smart Electricity Monitoring and Control System Using

- Usage Data”, *Wireless Communications and Mobile Computing*, Oct 2021
<https://www.hindawi.com/journals/wcmc/2021/6544649/>
4. H. Elsaraf, M. Jamil, **B. Pandey**, "Techno-Economic Design of a Combined Heat and Power Microgrid for a Remote Community in Newfoundland Canada", *IEEE Access*, Vol.9, pp. 91548-91563, June 2021 <https://ieeexplore.ieee.org/document/9464899>
 5. S. Narejo, **B. Pandey**, D. E. Vargas, C. Rodriguez and M. R. Anjum, “Weapon Detection Using YOLO V3 for Smart Surveillance System”, *Mathematical Problems in Engineering*, May 2021, Hindawi, <https://downloads.hindawi.com/journals/mpe/2021/9975700.pdf>
 6. M. K. Hasan, M. Shafiq, S. Islam, **B. Pandey**, Y. A. B. El-Ebiary, N. S. Nafi, C. Rodriguez, and D. E. Vargas, “Lightweight Cryptographic Algorithms for Guessing Attack Protection in Complex Internet of Things Applications”, *Complexity*, April 2021, Hindawi <https://www.hindawi.com/journals/complexity/2021/5540296/>
 7. S. Islam, O. O. Khalifa, A. H. A. Hashim, M. K. Hasan, M. A. Razzaque & **B. Pandey**, “Design and Evaluation of a Multihoming-Based Mobility Management Scheme to Support Inter Technology Handoff in PNEMO”, *Wireless Personal Communications*, Vol. 114, pp. 133–1153, May 2020 <https://link.springer.com/article/10.1007/s11277-020-07412-0>
 8. M. K. Hasan, M. M. Ahmed, A. H. A. Hashim, A. Razzaque, S. Islam and **B. Pandey**, “A Novel Artificial Intelligence Based Timing Synchronization Scheme for Smart Grid Applications” *Wireless Personal Communications*, Vol. 114, pp. 1067–1084, April 2020, <https://link.springer.com/article/10.1007/s11277-020-07408-w>
 9. A. Bahnasse, F. E. Louhab, A. Khiat, A. Badri, M. Talea & **B. Pandey**, “Smart Hybrid SDN Approach for MPLS VPN Management and Adaptive Multipath Optimal Routing”, *Wireless Personal Communications*, Vol. 114, pp. 1107-1131, April 2020. <https://link.springer.com/article/10.1007/s11277-020-07411-1>
 10. A. Bahnasse, F. E. Louhab, A. Khiat, M. Talea, A. Badri, **B. Pandey**, “Novel Approach for Management of Automated IPv6 Network Simulation”, *Wireless Personal Communications*, Vol. 111, pp. 1487–1504, April 2020. <https://link.springer.com/article/10.1007/s11277-019-06928-4>
 11. **B. Pandey**, N. Pandey, A. Kaur, D. M. A. Hussain, B. Das and G. S. Tomar, “Scaling of Output Load in Energy Efficient FIR Filter for Green Communication on Ultra-Scale FPGA”, *Wireless Personal Communications*, June 2019, Volume 106, No. 4, pp 1813–1826. <https://link.springer.com/article/10.1007/s11277-018-5717-2>
 12. **B. Pandey**, G.A. Farulla, M. Indaco, L. Iovino and P. Prinetto, “Design and Review of Water Management System using Ethernet, Wi-Fi 802.11n, Modbus, and other communication standards”, *Wireless Personal Communications*, June 2019, Volume 106, No. 4, pp 1677–1699. <https://link.springer.com/article/10.1007/s11277-018-5380-7>
 13. G. Verma, T. Singhal, R. Kumar, S. Chauhan, S. Shekhar, **B. Pandey** & D. M. A. Hussain, “Heuristic and Statistical Power Estimation Model for FPGA Based Wireless Systems”, *Wireless Personal Communications*, Vol. 106, pages. 2087–2098, June 2019 <https://link.springer.com/article/10.1007/s11277-018-5927-7>
 14. D. Gupta, V. Gupta, M. Chandra & **B. Pandey**, “Performance Analysis of MSFRLS-VFF Based Real-Time Adaptive Noise Canceller with RLS and APA Algorithms Using TMS320C6713 Processor”, *Wireless Personal Communications*, Vol. 106, pp. 2113–2127, June 2019 <https://link.springer.com/article/10.1007/s11277-018-5929-5>
 15. B. Das, M. F. L. Abdullah, **B. Pandey**, D M A. Hussain, "Design and implementation of optical signal reinstatement technique for high DPSK RZ transceiver scheme”, *Wireless Personal Communications*, Vol. 106, pp. 1767-1786, June 2019, <https://link.springer.com/article/10.1007/s11277-018-5605-9>

16. M. R. M. V. Manickam, M. Mohanapriya, **B. Pandey**, S. Akhade, S. A. Kale, R. Patil, M. Vigneshwar, “Map-Reduce framework-based cluster architecture for academic student’s performance prediction using cumulative dragonfly based neural network”, Cluster Computing January 2019, Volume 22, Supplement 1, pp 1259–1275, <https://link.springer.com/article/10.1007/s10586-017-1553-5>
17. **B. Pandey**, B. Das, A. Kaur, T. Kumar, A. M. Khan, D M A. Hussain, G. S Tomar, “Performance Evaluation of FIR Filter After Implementation on Different FPGA and SOC and Its Utilization in Communication and Network”, Wireless Personal Communications, 2017, Vol.95, No. 2, pp 375–389. <https://link.springer.com/article/10.1007/s11277-016-3898-0>
18. K. Goswami, **B. Pandey**, T. Kumar, D M A. Hussain, “Different I/O Standard and Technology Based Thermal Aware Energy Efficient Vedic Multiplier Design for Green Wireless Communication on FPGA”, Wireless Personal Communications, 2017, Volume 96, No. 2, pp 3139–3158. <https://link.springer.com/article/10.1007/s11277-017-4345-6>
19. G. Verma, M. Kumar, V. Khare & **B. Pandey**, “Analysis of Low Power Consumption Techniques on FPGA for Wireless Devices”, Wireless Personal Communications, 95 (2), pp. 353-364, 2017 <https://link.springer.com/article/10.1007/s11277-016-3896-2>
20. B. Das, M. F. L. Abdullah, N. S. M. Shah, L. M. A. Ahmed & **B. Pandey**, “Development of New All-Optical Signal Regeneration Technique”, Wireless Personal Communications, 95 (2), pp. 523-537, 2017. <https://link.springer.com/article/10.1007/s11277-016-3907-3>
21. M. K. Hasan, A. F. Ismail, S. Islam, W. Hashim & **B. Pandey**, “Dynamic Spectrum Allocation Scheme for Heterogeneous Network”, Wireless Personal Communications, 95 (2), pp. 299-315, 2017. <https://link.springer.com/article/10.1007/s11277-016-3893-5>
22. S. H. A. Musavi, B. S. Chowdhry, T. Kumar, **B. Pandey** & W. Kumar, “IoT’s Enable Active Contour Modeling Based Energy Efficient and Thermal Aware Object Tracking on FPGA”, Wireless Personal Communications, 85 (2), pp. 529-543, 2015. <https://link.springer.com/article/10.1007/s11277-015-2753-z>
23. T. Kumar, **B. Pandey**, S. H.A. Musavi, N. Zaman, “CTHS Based Energy Efficient Thermal Aware Image ALU Design on FPGA”, Wireless Personal Communications, Vol.83, No.1, July 2015. <https://link.springer.com/article/10.1007/s11277-015-2801-8>
24. T. Kumar, **B. Pandey**, T. Das, and B. S. Chowdhry, "Mobile DDR IO Standard Based High Performance Energy Efficient Portable ALU Design on FPGA", Wireless Personal Communications, Vol. 76, No. 3, 2014, pp 569-578. <https://link.springer.com/article/10.1007/s11277-014-1725-z>

Selected 20+ Publication in SCOPUS: Total 150 Articles in Scopus

Refer this link for 150 Articles in Scopus Profile:

<https://www.scopus.com/authid/detail.uri?authorId=57203239026>

1. **B. Pandey**, N. Bisht, S. Budhani, “Comparative Performance Analysis of AES Encryption Algorithm for Various LVC MOS on Different FPGAs”, World Journal of Engineering, Emerald (Accepted)
2. P. Singh, **B. Pandey**, N. Bhandari, S. Bisht and N. Bisht, “Output Load Capacitance Scaling Based Energy Efficient Design of ROM on 28nm FPGA”, Second International Conference on Computing, Communication and Cyber Security (IC4S-2020), October 2020. Lecture Notes in Networks and Systems (LNSS), 2021, Springer. https://link.springer.com/chapter/10.1007/978-981-16-0733-2_69
3. **B. Pandey**, V. Bisht, S. Ahmad, I. Kotsyuba, “Increasing Cyber Security by Energy Efficient Implementation of DES Algorithms on FPGA”, Journal of Green Engineering, Vol. 11, No. 1, 2021, pp 72-87 <http://www.jgenng.com/volume11-issue1.php>

4. **B. Pandey**, G. S. Tomar, R. S. Bhadoria, D. M. A. Hussain, Ciro Rodriguez Rodriguez, "Environment Friendly FSM Design on Ultrascale Architecture: An Energy Efficient Green Computing Approach", World Journal of Engineering, Vol. 18 No. 4, pp. 553-557, 2021
<https://www.emerald.com/insight/content/doi/10.1108/WJE-08-2020-0397/full/html>
5. **B. Pandey**, R. R. Mohamed, G. S. Tomar, D. M. A. Hussain, Y. A B. El-Biary, "FSM Based Green Memory Design and its Implementation on Ultrascale Plus FPGA", Journal of Critical Reviews, ISSN- 2394-5125, Vol. 7, No. 19, pp.454-458, 2020
<http://www.jcreview.com/fulltext/197-1594819368.pdf?1596969111>
6. **B. Pandey**, K. Kumar, S. Ahmad, A. K. Pandit, D. Singh, D M A Hussain, "Leakage power consumption of address register interfacing with different families of FPGA", International Journal of Innovative Technology and Exploring Engineering (IJITEE), Vol. 8, Issue- 9S2, July 2019
<https://www.ijitee.org/wp-content/uploads/papers/v8i9S2/I11080789S219.pdf>
7. **B. Pandey**, G. A. Farulla, M. Indaco, and P. Prinetto, "Survey of Computational Ability of Sensors-Actuators in Water Management Cyber-Physical System", Journal of Advanced Research in Dynamical and Control Systems, Vol. 10, 04-Special Issue, 2018,
<http://www.jardcs.org/backissues/abstract.php?archiveid=4809>
8. **B. Pandey**, V. Jain, R. Sharma, M. Yadav and D M A Hussain, "Scaling of supply voltage in design of energy saver FIR filter on 28nm FPGA", International Journal of Control and Automation, 10 (12), pp. 77-88, 2017. http://article.nadiapub.com/IJCA/vol10_no12/7.pdf
9. **B. Pandey**, M. A. Rahman, D. M. A. Hussain, A. Saxena and B. Das, "Leakage power reduction with various IO standards and dynamic voltage scaling in vedic multiplier onvirtex-6 FPGA", Indian Journal of Science and Technology, 9 (25), art. no. 97157, 2016.
<https://indjst.org/articles/leakage-power-reduction-with-various-io-standards-and-dynamic-voltage-scaling-in-vedic-multiplier-on-virtex-6-fpga>
10. **B. Pandey**, S. Pandey, S. Sharma, K. Kalia, K. Nanda and D M A Hussain, "Clock gating based energy efficient and thermal aware design for Vedic equation solver on 28nm and 40nmFPGA", International Journal of Control and Automation, 9 (7), 2016, pp. 101-112.
http://article.nadiapub.com/IJCA/vol9_no7/10.pdf
11. B. Das, M. F. L. Abdullah, M.S.N. Shahida, Q. Bakhsh, **B. Pandey**, "Energy efficient design of laser driver using field programming gate array", ARPN Journal of Engineering and Applied Sciences, Vol. 11, No. 6, 2016, pp. 3796-3804.
http://www.arpnjournals.org/jeas/research_papers/rp_2016/jeas_0316_3881.pdf
12. G. Verma, S. Maheshwari, S. Kaur, N. Baishander, I. Singh **B. Pandey**, "Low power squarer design using Ekadhikena Purvena on 28nm FPGA", International Journal of Control and Automation, Vol. 9, No. 5, pp. 281-288, 2016.
http://article.nadiapub.com/IJCA/vol9_no5/27.pdf
13. **B. Pandey**, V. Thind, S. K. Sandhu, T. Walia and S. Sharma, "SSTL based power efficient implementation of DES security algorithm on 28nm FPGA", International Journal of Security and its Applications, 9 (7), 2015, pp. 267-274.
http://article.nadiapub.com/IJSIA/vol9_no7/23.pdf
14. S. Madhok, **B. Pandey**, A. Kaur, M. H. Minver, D. M. A. Hussain, "HSTL IO standard based energy efficient multiplier design using Nikhilam navatashcaramam dashatah on 28nm FPGA", International Journal of Control and Automation, Vol. 8, No. 8, 2015, pp. 35-44.
http://article.nadiapub.com/IJCA/vol8_no8/5.pdf
15. D. Singh, K. Garg, R. Singh, **B. Pandey**, K. Kalia, H. Noori, "Thermal aware internet of things enable energy efficient encoder design for security on FPGA", International Journal of Security and its Applications, Vol. 9, No. 6, pp. 271-278, 2015.
http://article.nadiapub.com/IJSIA/vol9_no6/26.pdf
16. A. Moudgil, K. Garg, **B. Pandey**, "Low voltage complementary metal oxide semiconductor based internet of things enable energy efficient RAM design on 40nm and 65nm FPGA",

- International Journal of Smart Home, Vol. 9, No. 9, pp. 37-50, 2015. <https://www.scopus.com/sourceid/20200195004>
17. B. Das, M. Abdullah, M. S. Nor Shahida, Q. Bakhsh, **B. Pandey**, “Power optimization of semiconductor laser driver using voltage scaling technique”, ARPN Journal of Engineering and Applied Sciences, Vol. 10, No. 18, pp. 8379-8387, 2015. http://www.arpnjournals.org/jeas/research_papers/rp_2015/jeas_1015_2730.pdf
 18. S. Nagah, **B. Pandey**, K. Kalia, R. Kaur, S. Rahman, Mahbub-E-Noor, “IO standards based on green communication using fibonacci generator design on FPGA”, International Journal of Control and Automation, Vol. 8, No. 8, pp. 113-118, 2015. http://article.nadiapub.com/IJCA/vol8_no8/13.pdf
 19. T. Kumar, **B. Pandey**, S.M.M. Islam, N. Singh, S. M. Alam, T. Das, “Mapping based energy efficient counter design on FPGA”, Advanced Materials Research, Vol 984-985, 2014, pp. 1085-1088. <https://www.scientific.net/AMR.984-985.1085>
 20. **B. Pandey**, T. Kumar, T. Das, S.M.M. Islam, J. Kumar, “Thermal mechanics-based energy efficient FIR filter for digital signal processing”, Applied Mechanics and Materials, 612, pp. 65-70, 2014. <https://www.scientific.net/AMM.612.65>
 21. S.M.M. Islam, **B. Pandey**, M. Mahbub-E-Noor, S.M.T. Siddiquee, S. Jaiswal, “Simulation of voltage scaling aware mobile battery charge controller sensor on FPGA”, Advanced Materials Research, 893, pp. 798-802, 2014. <https://www.scientific.net/AMR.893.798>

16 Conference Proceeding with ISBN

- Details are available on <http://gyancity.com/books.html>

Selected 23 Conference Publications out of Total 82 Conferences in 23 Different Countries

1. N. Bhoj, M. Khari, B. Pandey, “Improved Identification of Negative Tweets related to Covid-19 Vaccination by Mitigating Class Imbalance”, 13th International Conference on Computational Intelligence and Communication Networks (CICN), 22-23 September, 2021, National University of Federico Villareal, **Peru** <https://ieeexplore.ieee.org/document/9574664>
2. B. Pandey, V. Bisht, D. M. A. Hussain, M. Jamil and M. K. Hasan, “Energy-efficient implementation of AES algorithm on 16nm FPGA”, The International Conference on Communication Systems and Network Technologies (CSNT-2021), Bhopal, **India**, IEEE <https://ieeexplore.ieee.org/document/9509662/>
3. K. Kumar, **B. Pandey**, D M A. Hussain, “Leakage Power Consumption of Address Register Interfacing with Different Families of FPGA”, 5th International Conference on Green Computing and Engineering Technologies (ICGCET), 17-19 September 2019, Hotel Diwan, 31 Boulevard Hassan Seghir, Casablanca 20000, **Morocco**. http://icgcet.org/ICGCET_2019.pdf
4. D M A. Hussain, **B. Pandey**, A. Kumar, J. Levy, P. Kumar, B. S. Chowdhry, A. Jain, “Energy Efficient and High Performance FIR Filter Design on Spartan-6 FPGA”, 2nd International Multi-Topic Conference on Engineering and Science (IMCES'19), 05-07 May 2019, Holiday Inn Mauritius Mon Tresor, Mon Tresor, Plaine Magnien, **Mauritius**. http://imces.tech/IMCES_2019.pdf
5. A. Kumar, **B. Pandey**, D M A. Hussain, M. A. Rahman, V. Jain, A. Bahanasse, “Frequency Scaling and High Speed Transceiver Logic Based Low Power UART design on 45nm FPGA”, 11th International Conference on Computational Intelligence and Communication Networks (CICN), January 2019, Honolulu, **USA** <https://ieeexplore.ieee.org/document/8902375>
6. **B. Pandey**, P. Sharan, L. L. Dhirani and D. M. A. Hussain, "Role of Scaling of Frequency and Toggle Rate in POD IO Standards Based Energy Efficient ALU Design on Ultra Scale FPGA",

- 10th International Conference on Computational Intelligence and Communication Networks (CICN), Esbjerg, Denmark, 2018, pp. 50-53. <https://ieeexplore.ieee.org/document/8864933>
7. **B. Pandey**, V. Jain, R. Sharma, M. Yadav, D M A Hussain, "Scaling of Supply Voltage in Design of Energy Saver FIR Filter on 28nm FPGA", **3rd International conference** on Recent Trends in Computer Science and Electronics Engineering (RTCSE'18), 02-03 January 2018, Bangkok, **Thailand** http://www.rtcse.org/RTCSE_2018.pdf
 8. N. Pandey, **B. Pandey**, D M A Hussain, "Reduction in Power Consumption of Packet Counter on VIRTEX-6 FPGA by Frequency Scaling", **IEEE Saudi Arabia Smart Grid Conference (SASG 2017)**, 12th - 14th Dec, 2017, Jeddah Hilton Hotel, Jeddah, **Saudi Arabia**. <https://ieeexplore.ieee.org/document/8356479>
 9. V. Thind, N. Pandey, **B. Pandey**, and D. M. A. Hussain, "Stack Memory Implementation and Analysis of Timing Constraint, Power and Memory using FPGA", **IEEE 9th International Conference on Computational Intelligence and Communication Networks (CICN)**, 16-17 September 2017, Final International University, **Cyprus**. <https://ieeexplore.ieee.org/document/8319388>
 10. A. Khanna, B. Das, **B. Pandey**, DMA Hussain, V. Jain, "A Discussion about Upgrading the Quick Script Platform to Create Natural Language based IoT Systems", 2nd International Conference on Recent Trends in Computer Science and Electronics Engineering (RTCSE), 02-03 January 2017, **Kuala Lumpur, Malaysia** http://rtcse.org/RTCSE_2017.pdf
 11. V. Thind, **B. Pandey**, D M A Hussain, "Power Analysis of Energy Efficient des Algorithm and Implementation on 28nm FPGA ", IEEE 15th International Symposium on Distributed Computing and Applications to Business, Engineering and Science (DCABES), 24-26 August 2016, Paris, **France** <http://ieeexplore.ieee.org/document/7982309/>
 12. K. Kalia, **B. Pandey**, D M A Hussain, "SSTL Based Thermal and Power Efficient RAM Design on 28nm FPGA for Spacecraft", IEEE 2016 International Conference on Smart Grid and Clean Energy Technologies, 19-22 October 2016, University of Electronic Science and Technology of China (UESTC), Chengsu, **China**. <http://ieeexplore.ieee.org/document/7876075/>
 13. K. Garg, A. Moudgil, B. Das, M F L Abdullah, **B. Pandey**, D M A Hussain, "GTL Based Internet of Things Enable Processor Specific RAM Design on 65nm FPGA", IEEE 57th International Symposium ELMAR-2015. 28-30 September 2015, Zadar, **Croatia**: the oldest conference in Europe. <https://ieeexplore.ieee.org/document/7334514>
 14. **B. Pandey**, V. Thind, S. K. Sandhu, T. Walia, S. Sharma "SSTL Based Power Efficient Implementation of DES Security Algorithm on 28nm FPGA", Scopus International conference on Green Computing and Engineering Technology 2015 (ICGCET'15), 25-26 July 2015, Dubai, **UAE**. http://www.icgct.org/ICGCET_2015.pdf
 15. K. Kalia, **B. Pandey**, A. Kaur, K. Nanda, S. Malhotra, D.M.A Hussain, "Pseudo Open Drain IO Standard Based Energy Efficient Solar Charge Sensor Design on 20nm FPGA", 11th IEEE International Conference on Power Electronics and Drive Systems (PEDS), Sydney, **Australia**, 9 – 12 June 2015. <https://ieeexplore.ieee.org/document/7203530>
 16. B. Das, M.F.L Abdullah, M. S. N. Shahida, and Q. Bukhsh, **B. Pandey**, "Temperature Control of Pseudo Noise Generator Based Optical Transmitter using Airflow and Heat Sink Profile at High Speed Transceiver Logic IO Standard", Int. Conf. on Sensors, Materials and Manufacturing (ICSMM), Ho-Chi-Minh, **Vietnam**, February 2015. <http://www.joace.org/uploadfile/2015/0529/20150529042946338.pdf>
 17. **B. Pandey**, A. Kaur, T. Kumar, T. Das, A. Rahman, D M A Hussain, "LVTTTL Based Energy Efficient Watermark Generator Design and Implementation on FPGA", IEEE International Conference on ICT Convergence (ICTC), 22-24 October 2014, Busan, **Korea**, <https://ieeexplore.ieee.org/document/6983240>
 18. R Saini, N Bansal, M Bansal, L Kalra, P M Singh, **B Pandey**, D. M. A. Hussain "Ambient Temperature Based Thermal Aware Energy Efficient ROM Design on FPGA", Scopus 4th

- International Conference on Advanced Materials and Engineering Materials (ICAMEM), 19-20 October 2014, **Hong Kong**. <http://www.scientific.net/AMR.1082.467>
19. T. Kumar, **B. Pandey**, T. Das, D. M. A. Hussain, "IO Standard Based Thermal/Energy Efficient Green Communication For Wi-Fi Protected Access on FPGA", 6th IEEE International Congress on Ultra-Modern Telecommunications and Control systems and Workshops (ICUMT), St. Petersburg, **Russia**, 06-08 October 2014, <https://ieeexplore.ieee.org/document/7002085>
 20. T. Kumar, **B. Pandey**, T. Das, S. Thakur, B. S. Chowdhry, "Frequency, Voltage and Temperature Sensor Design for Fire Detection in VLSI Circuit on FPGA", Springer International Multitopic Conference (IMTIC'13), December, 2013, Mehran University, Sindh, **Pakistan**, http://link.springer.com/chapter/10.1007/978-3-319-10987-9_12
 21. T. Kumar, **B. Pandey**, T. Das, M. H. Minver, "LVDCI I/O Standard Based Green Image ALU Design on Ultra Scale FPGA", IEEE 8th International Conference on Industrial and Information Systems (ICIIS), University of Peradeniya, Kandy, **Srilanka**, pp. 283 - 288, December 2013 <https://ieeexplore.ieee.org/document/6731996>
 22. **B. Pandey** and M. Pattanaik, "Clock Gating Aware Low Power ALU Design and Implementation on FPGA", International Conference on Network and Computer Science (ICNCS), **Singapore**, April, 2013, <http://www.ijfcc.org/papers/206-S2013.pdf>
 23. **B. Pandey**, and M. Pattanaik, "Drive Strength and LVCMOS Based Dynamic Power Reduction of ALU on FPGA", International Conference on Information Technology and Science (ICITS 2013), Bali, **Indonesia**, March 16-17, 2013 <http://www.lnit.org/uploadfile/2013/0506/20130506020243704.pdf>

Patent

Invention Title	GPHL IO STANDARDS BASED ENERGY EFFICIENT VEDIC MULTIPLIER DESIGN ON FPGA
Application Number, Date	3064/DEL/2014, 28/10/2014
Journal, Issue Number, Year	The Patent Office Journal, 23, 2016
Publisher	Office of the Controller General of Patents, The Government of India
Publication Link	http://www.ipindia.nic.in/writereaddata/Portal/IPOJournal/1_347_1/part1.pdf
Invention Title	ENERGY EFFICIENT UNICODE READER DESIGN ON FPGA
Application Number, Date	2849/DEL/2014, 07/10/2014
Journal, Issue Number, Year	The Patent Office Journal, 22, 2016
Publisher	Office of the Controller General of Patents, The Government of India
Publication Link	http://www.ipindia.nic.in/writereaddata/Portal/IPOJournal/1_346_1/part1.pdf

Contribution in 80+ International Conference

1. **PROGRAM COMMITTEE CHAIRS** in 5th International Multi-Topic Conference on Engineering and Science (IMCES), Aalborg University, Esbjerg, 29-30 June 2022, **Denmark**.
2. **GENERAL CHAIR** in International Conference on Business, Management, Environmental, and Social Science (BMESS), 30-31 March 2022, Dubai, UAE
3. **GENERAL CHAIR in IEEE** 1st International Conference on AI in Cybersecurity (ICAIC), 15-17 March 2022, University of Houston-Victoria, USA
4. **PROGRAM COMMITTEE CHAIRS** in 7th International Conference on Recent Trends in Computer Science and Electronics (RTCSE), January 5-7, 2022, University of Hawaii, **USA**.
5. **Technical Program Committee Member** in International Conference on Communication & Artificial Intelligence (ICCAI-2021), 19-20 November, 2021, GLA Univesity, Mathura
6. **PUBLICATION COMMITTEE CHAIR** in 13th International Conference on Computational Intelligence and Communication Networks (CICN), 22-23 September 2021, Universidad Nacional Federico Villarreal, Lima, **Peru**
7. **PROGRAM COMMITTEE CHAIRS** in 7th International Conference on Green Computing and Engineering Technologies (ICGCET®), Universidad Nacional Federico Villarreal, Lima, 22 Sep - 23 Sep 2021, **Peru**.
8. **PROGRAM COMMITTEE CHAIRS** in 4th International Multi-Topic Conference on Engineering and Science (IMCES), Faculty of Information Technology, Universitas YARSI, Jakarta, 29-30 June 2021, **Indonesia**.
9. **Technical Program Committee Member in IEEE** 10th International Conference on Communication Systems and Network Technologies (CSNT), MIR Lab Gwalior, 18-19 June, 2021, Bhopal, India.
10. **PROGRAM COMMITTEE CHAIRS** in 6th International Conference on Recent Trends in Computer Science and Electronics (RTCSE), January 5-7, 2021, University of Hawaii, **USA**.
11. **SESSION CHAIR** in 12th International Conference on Computational Intelligence and Communication Networks (CICN), 25-26 September 2020, Birla Institute of Applied Science, Bhimtal, **India**
12. **PROGRAM COMMITTEE CHAIRS** in 6th International Conference on Green Computing and Engineering Technologies (ICGCET®), Herzen State Pedagogical University of Russia, St Petersburg, 16 Sep - 18 Sep 2020, **Russia**.
13. **Designated Reviewer** in Conference on Internet of Things and Embedded Intelligence (CITEI 2020), Yogyakarta, **Indonesia** on July 29-30, 2020
14. **International Advisory Committee** in Second International Conference on Emerging Trends in Information Technology- 2020, Institute of Information Technology and Management, Janakpuri, Delhi, 19-20 June, 2020, **India**.
15. **PROGRAM COMMITTEE CHAIRS** in 3rd International Multi-Topic Conference on Engineering and Science (IMCES), Faculty of Information Technology, Universitas YARSI, Jakarta, 27-29 May 2020, **Indonesia**.
16. **PROGRAM COMMITTEE CHAIRS** in 5th International Conference on Recent Trends in Computer Science and Electronics January 7-9, 2020 Hawaii, **USA**.
17. **Industrial Liaison Committee Chair** in 5th International conference on Green Computing and Engineering Technology 2019 (ICGCET), 07-09 Sep 2019, **Morocco**.
18. **International Advisory Committee Member in International Conference on Emerging Trends in Information Technology (ICETIT-2019), June 21-22, 2019, IITM Delhi, India**.
19. **Programme Committee Chair** in International Multi-Topic Conference on Engineering and Science (IMCES), 5-7 May 2019, Holiday Inn Mauritius Mon Tresor, Mon Tresor, **Plaine Magnien, Mauritius**.
20. **Paper Presenter** at 2019 IEEE International Conference on Electrical, Computer and Communication Technologies (ICECCT), February 2019, Coimbatore, India

21. **Programme Committee Chair** in 4th International conference on Recent Trends in Computer Science and Electronics Engineering (RTCSE), 3-4 Jan 2019, **University of Hawaii, USA.**
22. **Finance Committee Chairs** in 11th International Conference on Computational Intelligence and Communication Networks (CICN), 3-4 Jan 2019, **University of Hawaii, USA.**
23. **Finance Committee Chairs** in 10th International Conference on Computational Intelligence and Communication Networks (CICN), 17-19 Sep 2018, **Aalborg University, Denmark.**
24. **Technical Programme Committee Member** in 4th International conference on Green Computing and Engineering Technology (ICGCET), 17-19 Sep 2018, **Aalborg University, Denmark.**
25. **Technical Programme Committee Member** in 3rd International conference on Recent Trends in Computer Science and Electronics Engineering (RTCSE), 02-03 January 2018, Bangkok, Thailand
26. **Paper Presenter** at IEEE International Conference on Advanced Computing, ICAC, 14-16 December 2017, Chennai, India
27. **Paper Presenter** at IEEE Saudi Arabia Smart Grid Conference (SASG 2017), 12th - 14th Dec, 2017, Jeddah Hilton Hotel, Jeddah, **Saudi Arabia.**
28. **Technical Programme Committee Member** in IEEE International Conference on Data, Engineering and Applications 2017 (IDEA) is being organized during October 28-29, 2017 in Bhopal.
29. **Publicity Committee Chairs** in 9th International Conference on Computational Intelligence and Communication Networks (CICN), 16-17 Sep 2017, **Final International University, Cyprus**
30. **Technical Programme Committee Member** in 3rd International conference on Green Computing and Engineering Technology 2017 (ICGCET), 8-10 August 2017, Ireland.
31. **Organized International conference** on Recent Trends in Computer Science and Electronics Engineering (RTCSE), 02-03 January 2017 in Kuala Lumpur, Malaysia
32. **Paper Presenter** at IEEE 2016 International Conference on Smart Grid and Clean Energy Technologies, 19-22 October 2016, University of Electronic Science and Technology of China (UESTC), Chengsu, **China**
33. **Paper Presenter** at IEEE 15th International Symposium on Distributed Computing and Applications to Business, Engineering and Science (DCABES), 24-26 August 2016, Paris, **France**
34. **Organized International conference** on Green Computing and Engineering Technology (ICGCET), 18-20 August 2016, Denmark in association of **Aalborg University, MIR Lab and Gyancity Research Lab.**
35. **Designated Reviewer** on the Technical Program Committee for the International conference on Architectural Research & Computer-Aided Design (ICCRD), 20-22 April 2016, Crowne Plaza St. Peter's, Rome, Italy
36. **Session Chair** in IEEE International Conference on "Computing for Sustainable Global Development (INDIACOM), Bharati Vidyapeeth, Delhi, 16-18 March 2016.
37. **Publication Chair** in IEEE 6th International Conference on Communication Systems and Network Technologies (CSNT), Chitkara University, Chandigarh, 05-07 March, 2016.
38. **Designated Reviewer** on the Technical Program Committee for the IEEE International Conference on Teaching and Learning in Education (ICTLE), Universiti Tenaga Nasional, Malaysia, 1-2 March 2016.
39. **Organized Scopus Index International Conference** on Recent Trends in Computer Science and Electronics Engineering (RTCSE), 02-03 January 2016 in Kuala Lumpur, Malaysia
40. **Designated Reviewer** on the Technical Program Committee in Seventh International Conference on Computational Intelligence and Communication Networks 2015 (CICN), 12-14 Dec 2015

41. **Session Chair in** Springer 50th Golden Jubilee Annual Convention of Computer Society of India (CSI-2015) on Digital Life, 02nd – 05th December, 2015
42. **Paper Presenter at** Communication, Control and Intelligent Systems (CCIS), 7-8 November 2015, Mathura, India
43. **Designated Reviewer on the Technical Program Committee** in International Conference on Mechanics & Applied Physics (ICMAPH), 05-07 November, 2015 Rome, Italy.
44. **Paper Presenter at** IEEE 57th International Symposium ELMAR-2015. 28-30 September 2015, Zadar, **Croatia**: the oldest conference in Europe.
45. **Paper Presenter at** IEEE International Conference on Signal Processing, Computing and Control (ISPCC), 24-26 September 2015, Solan, India
46. **Technical Program Committee Member** in the International Conference on Computing in Mechanical Engineering (ICCME), 10-13 August, 2015, Kochi, India.
47. **Organized International Conference** on Green Computing and Engineering Technology 2015 (ICGCET), 25-26 July 2015, Dubai in association of **Aalborg University, MIR Lab and Gyancity Research Lab.**
48. **Designated Reviewer on the Technical Program Committee** in Intl. Conf. on Electronics, Computing and Communication Technologies (CONECCT) , Bangalore, 10-11 July, 2015.
49. **Paper Presenter at** 11th IEEE International Conference on Power Electronics and Drive Systems (PEDS), Sydney, **Australia**, 9 – 12 June 2015.
50. **Paper Presenter at** Power Generation System and Renewable Energy Technology (PGSRET), International Islamic University, Islamabad, 11-12 June 2015
51. **Designated Reviewer on the Technical Program Committee** in International Conference on Computer, Communication, and Control Technology (I4CT'), 21 - 23 April 2015, Kuching, Sarawak, Malaysia.
52. **Technical Program Committee Member in IEEE 5th Intl. Conference** on Communication Systems and Network Technologies (CSNT), MIR Lab Gwalior, 04-06 April, 2015.
53. **Session Chair in** 4th International Conference on Wireless Networks & Embedded Systems: An Approach towards Clean & Sustainable Technology, 20th-21st March, 2015, School Of Electronics and Electrical Engineering, Chitkara University, Punjab.
54. **Special Session Chair on Green Computing in IEEE 2nd International Conference** on Computing for Sustainable Global Development (INDIACOM), Bharati Vidyapeeth, Delhi, 11-13 March 2015.
55. **Paper Presenter at** Int. Conf. on Sensors, Materials and Manufacturing (ICSMM), Ho-Chi-Minh, **Vietnam**, February 2015.
56. **Paper Presenter at** IEEE International Conference on Signal Processing and Integrated Networks (SPIN), Noida, February 2015.
57. **Technical Program Committee Member** in IBMSGs: International Summit on Bio-Metrics and Smart Government, 18-20 January, 2015, Sousse, Tunisia.
58. **Technical Program Committee Member** in International Conference on Computer Vision and Image Analysis applications (ICCVIA' 2015), 18-20 January, 2015, Sousse, Tunisia.
59. **Paper Presenter at** International Conference on Open Source Systems and Technologies (ICOSST), 18-20 December 2014, **Lahore, Pakistan**
60. **Chair Associate** with Prof. Bhim Singh (Dean, Electrical Engineering, IIT Delhi) and many others in IEEE 9th International Conference on Industrial and Information Systems (ICIIS), IIIT Gwalior, 15-17 December, 2014
61. **Paper Presenter at** IEEE India International Conference on Power Electronics (IICPE), 8-10 December 2014, Kurukshetra, India
62. **Technical Program Committee Member** in International Conference on Computer Applications and Aided Diagnosis (ICCAAD), 25-26 November 2014, Algiers, Algeria

63. **Technical Program Committee Member** in International Conference on Control Automation & Applied Mechanics (ICCAAM), 25-26 November 2014, Algiers, Algeria
64. **Paper Presenter** at IEEE International Symposium on Environment Friendly Energies and Applications (EFEA), 19-21 November, St. Ouen, France, 2014
65. **Designated Reviewer** on the Technical Program Committee in Sixth International Conference on Computational Intelligence and Communication Networks (CICN), 08-10 November 2014
66. **Paper Presenter** at IEEE International Conference on ICT Convergence (ICTC), 22-24 October 2014, Busan, **Korea**,
67. **Paper Presenter** at Scopus 4th International Conference on Advanced Materials and Engineering Materials (ICAMEM), 19-20 October 2014, **Hong Kong**
68. **Paper Presenter** at 6th IEEE International Congress on Ultra-Modern Telecommunications and Control systems and Workshops (ICUMT), St. Petersburg, **Russia**, 06-08 October 2014
69. **Technical Program Committee Member** in IEEE International Conference on Signal Propagation and Computer Technology (ICSPCT), Government Engineering College, Ajmer, 12-14 July 2014
70. **Technical Program Committee Member** in IEEE Fourth International Conference on Communication Systems and Network Technologies (CSNT), NITTR Bhopal, 07-09 April, 2014.
71. **Proceeding Authors** in International Conference on Modern Communication & Computing Technologies (MCCT), QUEST Pakistan, 2014, 26-28 February, 2014.
72. **Paper Presenter** at International Conference on Optimization, Reliability, and Information Technology (ICROIT), 6-8 February 2014, Faridabad, India
73. **Paper Presenter** at International Conference on Computer Communication and Informatics (ICCCI), 3-5 January 2014, Coimbatore India
74. **Paper Presenter** at 2013 International Conference on Communication and Computer Vision (ICCCV), 20-21 December 2013, Coimbatore India
75. **Paper Presenter and Proceeding Authors in Springer** 3rd International Multi-topic Conference (IMTIC), Mehran University, 18-20 December, 2013.
76. **Paper Presenter and Member in International Review Panel** at IEEE 8th International Conference on Industrial and Information Systems (ICIIS), Kandy, Srilanka, 17-20 December, 2013.
77. **Paper Presenter** at IEEE India Conference (INDICON), 13-15 December, 2013, IIT Bombay, India
78. **Proceeding Authors in IEEE** International conference on Green Computing, Communication and Conservation of Energy (ICGCE), RMD Engineering College, 12-14 December, 2013.
79. **Paper Presenter** at 2013 INTERNATIONAL CONFERENCE ON SIGNAL PROCESSING AND COMMUNICATION (ICSC), Noida, India, 12-14 December, 2013.
80. **Reviewer in IEEE** 5th International Conference on Computational Intelligence and Communication Networks (CICN), GLA University, Mathura, 27-29 September, 2013.
81. **Reviewer in** Third International Conference on Advances in Computing and Information technology (ACITY), 13-15 July 2013.
82. **Paper Presenter** at IEEE Conference on Information & Communication Technologies (ICT), 11-12 April 2013, Thuckalay, India
83. **Paper Presenter** at 2013 IEEE International Conference on Energy Efficient Technologies for Sustainability, **10-12 April 2013**, Nagercoil, India
84. **Paper Presenter** at IEEE International Conference on Communication System and Network Technology (CSNT), 6-8 April 2013, Gwalior, **India**
85. **Paper Presenter** at International Conference on Network and Computer Science (ICNCS), Singapore, April, 2013

86. **Paper Presenter at International Conference on Information Technology and Science (ICITS 2013), Bali, Indonesia, March 16-17, 2013**